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CONTROLS**



Automation

UTICOR

DMS Multi-Axis Single-Turn Resolver Decoder Instruction & Operation Manual

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Multi-Axis Single-Turn Resolver Decoders

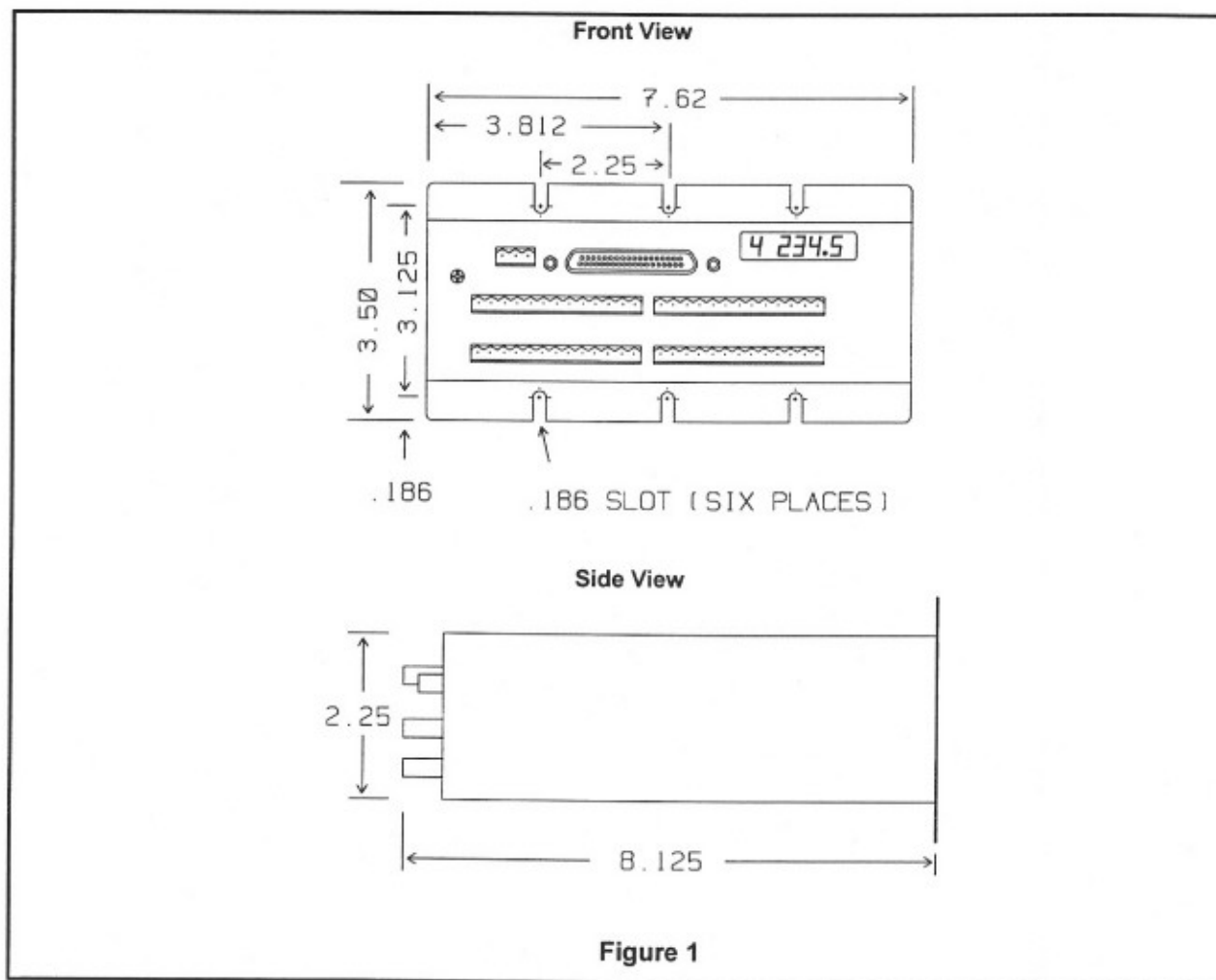
Instruction Manual

The DMS

Principle of Operation

The DMS decoders are available with either output or input multiplexing. The resolver analog signals are converted to the output digital format by a tracking resolver-to-digital converter. With input multiplexing, the analog signals are multiplexed to a single digital converter. In output multiplexing, multiple decoders are multiplexed to the parallel digital output port. The binary code at the input selects a par-

ticular resolver for output. Output multiplexing allows for a fast response and is recommended for high speed operations. In the input multiplexing versions, there is a 100 msec settling time after a change in resolver selection before the data is stable. The output multiplexing has only 15 μ sec settling time. Figure 1, below, shows the outline dimensions.



Variety of Outputs, Built-in PC-Handshake Permit Interface to any PLC

BCD, Natural Binary or Gray Code output formats are available with a choice of TTL, PNP source transistor or NPN sink transistor outputs. Optional PLC synchronization circuit provides an error-free method of interfacing output position data from the decoder to any programmable controller. Upon receipt of a data transfer command from the PLC, the stable data is latched and made available to the PLC.

Multiplexing Capability

The TTL type of outputs have multiplexing capability, that allows more than one decoder to be connected to the same control unit for multi-axis applications. For multiplexing PNP or NPN type outputs, consult the factory.

Up to 4096 Counts Per Turn Resolution at 1800 RPM

The DMS series resolver decoders are available with resolutions ranging from 256 to 4096 counts per turn to match unlimited control needs. The high performance ratiometric tracking converter assures exacting performance at speed up to 1800 rpm.

Highly Noise Immune Circuitry

The ratiometric tracking converter technique employed for resolver to digital decoding provides the best protection against electrical noise generated by power line transients, radio frequency interference and varying ground potentials. The ratiometric converter is practically immune to temperature variations and line frequency changes.

Optional optical isolation adds another layer of protection and enhances system integrity.

Extremely Rugged and Reliable Resolver as Position Transducer

The DMS series of resolver decoders combines the ruggedness of a resolver and reliability of an advanced solid-state control. The rugged heavy duty NEMA 13 (IP54) resolver can be mounted on a machine shaft in any hostile industrial environment, such as mechanical shock, vibrations, extreme humidity and temperature changes, oil mists, coolants, solvents, etc. The DMS decoder can be mounted up to 2500 feet from the resolver in a control panel.

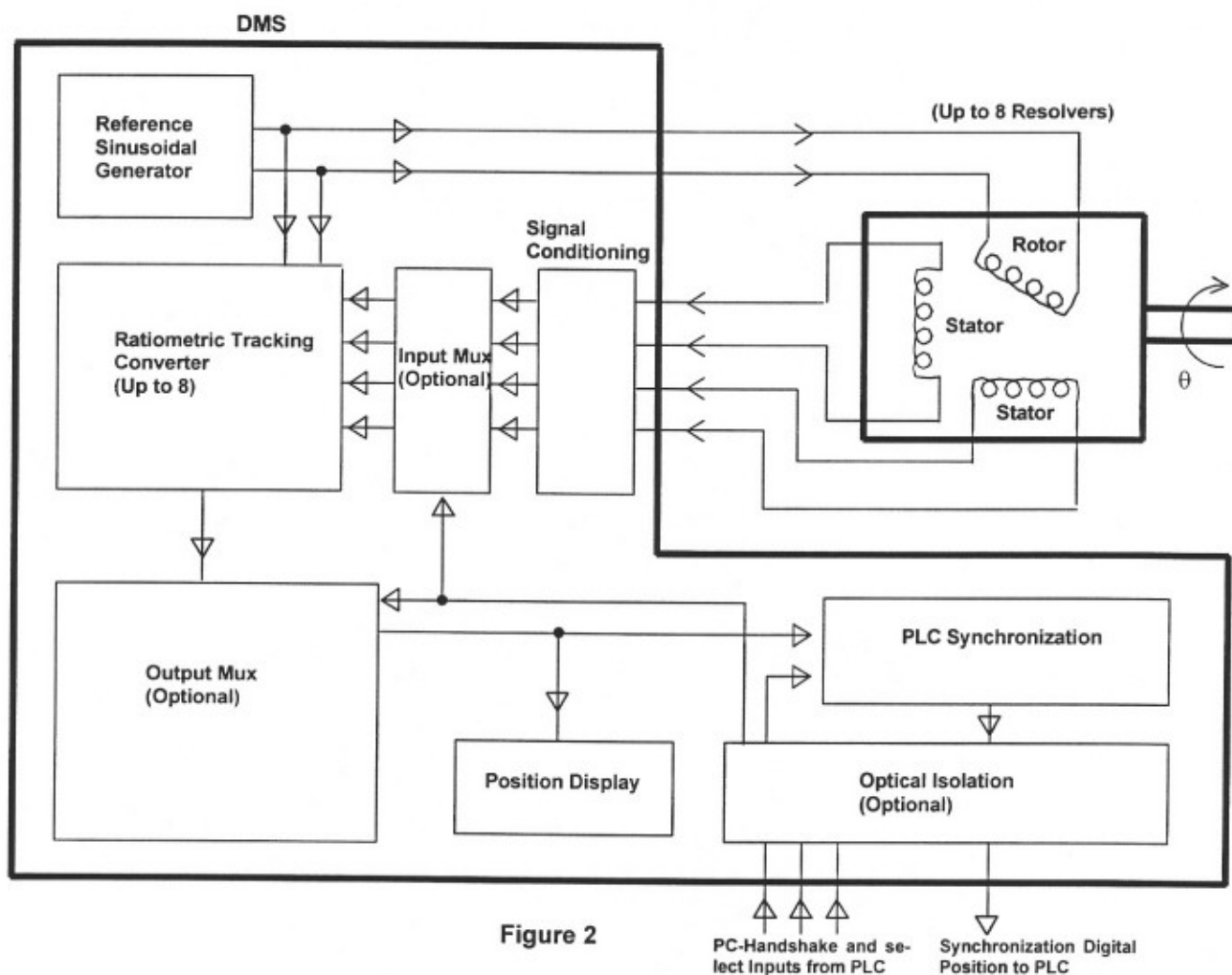


Figure 2

Specifications

Input Power

AC Power: 105–135 V or 210–270 V, 50/60 Hz, 5 W

DC Power: 8–30 VDC @ 0.25 A exclusive of load (without opto use, an external power supply is needed only for "P" outputs)

Operating Temperature: –10 to +130 °F (–23 to +55 °C)

Position Transducer:

Autotech's RL100, E6R, E7R, E8R or RL101 single turn resolvers.

Output Format and Resolution:

BCD: 360, 1000 or 3600 counts per turn

Natural Binary: 1000, 1024, 3600 or 4096 counts per turn

Gray code: 256, 360, 512, 1000, 1024, 3600 or 4096 counts per turn

Maximum Cable Length between Resolver and DMS:

2500 feet shielded (twisted pair)

Resolver shaft speed: 3600 RPM (Max)

Resolver to digital decoder tracking speed: 1800 RPM

Display: 3 or 4 digits, 0.25" LEDs

PLC Sync Input: (option)

Positive edge (Low to High) and

Negative edge (High to Low) triggered.

(See Input Logic for Voltage and Current specifications)

PC Handshake strobe: 30 µsec minimum width.

Strobe Delay to Latch Data: 100 µsec.

Transparent Mode/Microfreeze (Standard):

Output data is continuously updated at full speed. The data is latched for 100 / 10 µsec within 30 µsec of a transition (High to Low or Low to High) at data transfer input.

Optical Isolation (Optional):

2500 Volts, on all I/O's except resolver

Input Logic (Channel Select, P.L.C. Sync):

28VDC Max:

P type: Logic False, 0 to 0.8V @ 0.4 mA;

Logic True, 2.4V @ 3.2mA

N type: Logic False, 3.8V @ 0.4mA;

Logic True, 0 to 0.8V @ 3.2mA

C type: Logic False, 0 to 0.8V @ 3.2mA;

Logic True, 3.8V @ 0.4mA

0 to 5VDC Logic:

T type: Logic False, 0 to 0.8V @ 3.2mA;

Logic True, 2.4V @ 0.4mA

Channel Select Timing:

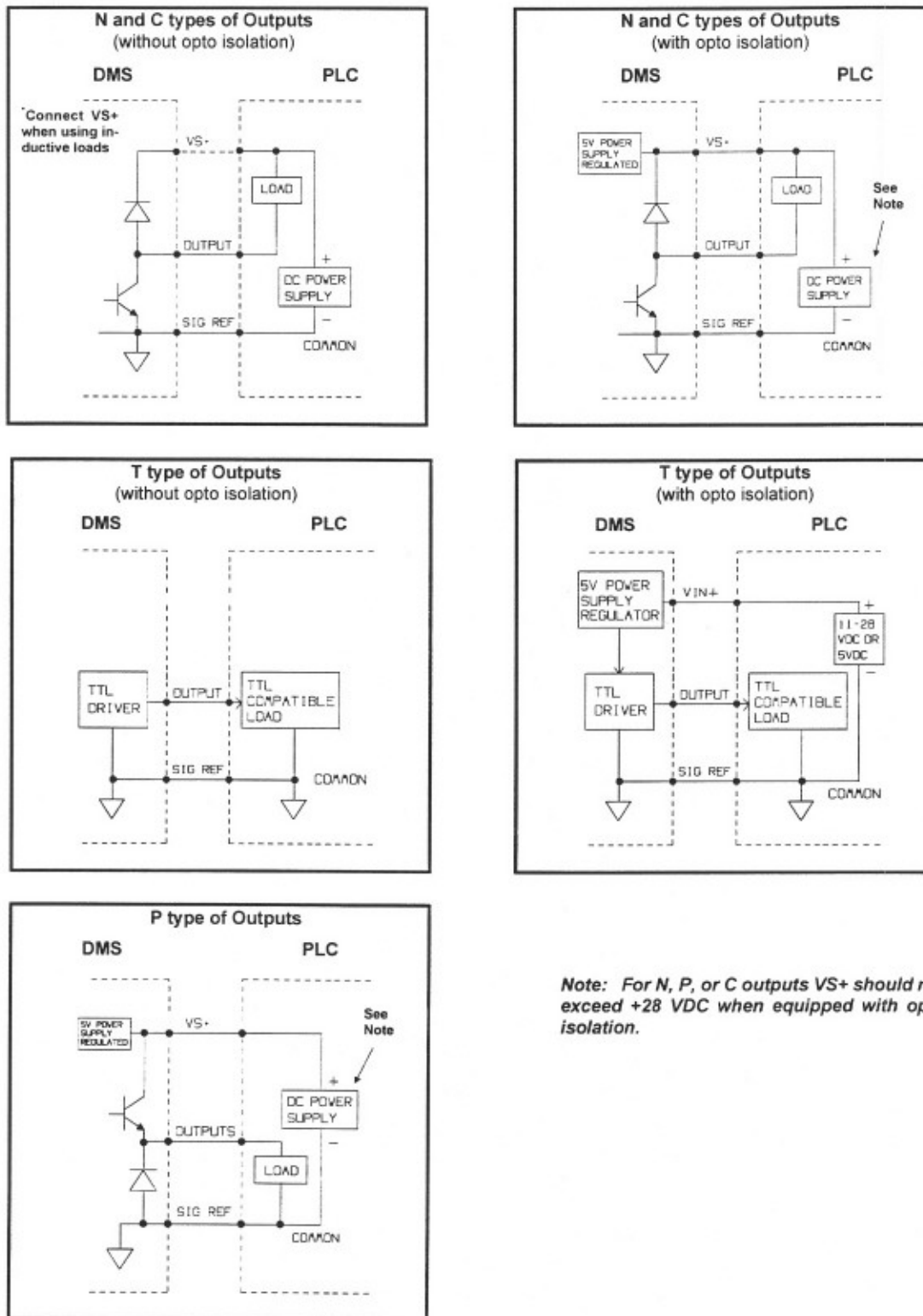
Input Multiplexing: 100 msec from channel select.

Output Multiplexing: 15 µsec from channel select.

Output Type	T: TTL* (74HC245)	P: PNP Source Transistor (Sprague UDN-2981A)	N: NPN Sink Transistor Low True (Sprague ULN-2803A)	C: NPN Sink Transistor High True (Sprague ULN-2803A)
Logic True	2 VDC @ 15 mA; 2.4 V @ 3 mA (20 µA leakage when Tristated)	Transistor ON 1.7 V drop @ 100 mA	Transistor ON 1.1 V @ 100 mA	Collector Open 0.1 mA leakage @ 50 VDC
Logic False	0.35 V @ 24 mA (0.4 mA leakage when Tristated)	Transistor OFF 0.2 mA leakage @ 50 V	Collector Open 0.1 mA leakage @ 50 VDC	Transistor ON 1.1 V @ 100 mA

*Note: Multiplexing or Tristating Input = Low active TTL level (i.e., Logic True: 0–0.8 V; Logic False: 2–5 VDC)

Typical Output Configurations



Note: For N, P, or C outputs VS+ should not exceed +28 VDC when equipped with opto isolation.

Figure 3

Installation and Operation

1. Position Transducer Mounting and Wiring:

Refer to the installation and wiring instructions for the transducer used in your application.

2. Decoder Mounting and Wiring:

2.1 Mounting

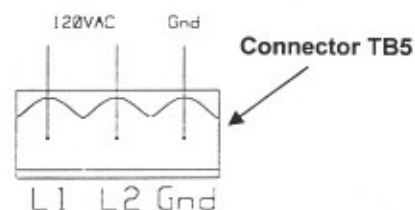
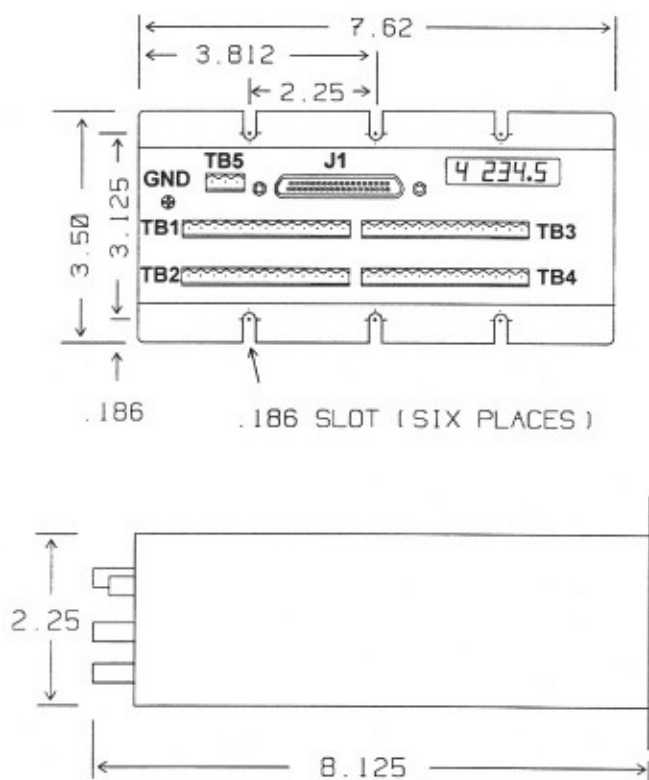
The DMS is designed to be mounted on a panel using the dimensions provided in this manual. In choosing the location, allow adequate wiring space in front of the DMS to eliminate mechanical stresses to cables and connectors. Also maintain cabinet internal temperature to be within DMS specifications.

2.2 Wiring

Wiring should always be done with system power removed. Wiring should be inspected for proper connections, absence of wire strands, and mechanical integrity of the terminal screws. All logic wiring, resolver connections, digital outputs and customer power ($V_s +/-$) must use shielded cable. Adequate separation must be maintained between the above cables and high voltage, high current, or any inductive wiring. Refer to the drawing on grounding and shielding requirements.

2.2.1 External Power and Resolver Connections:

Connect the 120VAC power input to L1 and L2 and earth ground to the 3-Pin Weidmuller connector TB5. See diagram and table below:



TB5 Connections		
Pin #	Terminal Name	Function
1	L1	AC Power
2	L2	AC Power
3	GND	AC Gnd

Figure 4

Connect the six wires from the resolver to the corresponding input channel number of Wiedmuller connectors TB1 through TB4. These terminals do not require lugs. See diagram and tables shown below:

**Typical Wiring Diagram for Resolver Connections
(Connector TB1 shown)**

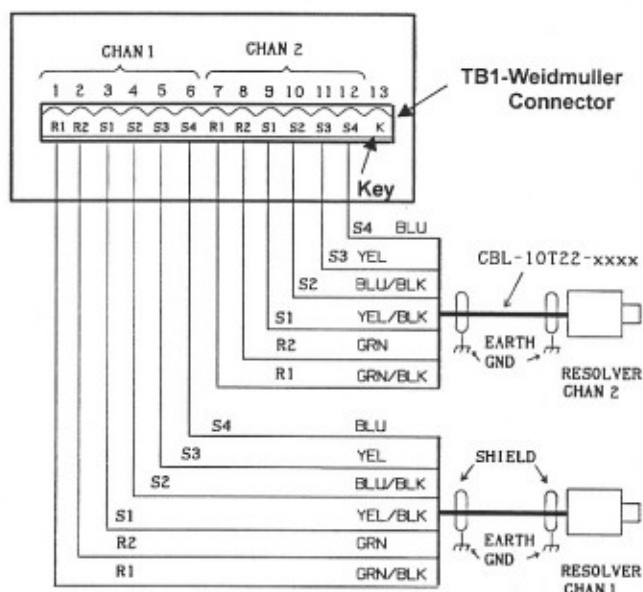


Figure 5

2.2.2 Shielding and Grounding

(Failure to observe any of these requirements may cause unpredictable operation and will void warranty)

1. All logic level wiring (including resolver and external power supply) must be done using overall foil shielded cables, with shields and equipment grounded as per figures 5 and 7. See *How to Order* section for suitable cables offered by Autotech.

2. Resolver shielded cable must consist of twisted pairs, and the twisted pairs must be wired as per wiring instructions. See *How to Order* section for a suitable resolver cable offered by Autotech.

It is recommended that the resolver shielded cable be run in its own separate conduit.

3. All ground planes on which the DMS and all external equipment are mounted must be held to the same RF potential, by good metallic connections to building frames, conduit or wiring trays.

4. All shielded cable must be kept at a minimum distance of 2 inches from all high voltage or inductive load wiring.

5. All shielded cable must be kept at a minimum distance of 12 inches from all motor wiring controlled by AC or DC drives.

TB1	
PIN #	WIRE (RES #)
1	R1 (1)
2	R2 (1)
3	S1 (1)
4	S2 (1)
5	S3 (1)
6	S4 (1)
7	R1 (2)
8	R2 (2)
9	S1 (2)
10	S2 (2)
11	S3 (2)
12	S4 (2)
13	KEY

TB2	
PIN #	WIRE (RES #)
1	R1 (3)
2	R2 (3)
3	S1 (3)
4	S2 (3)
5	S3 (3)
6	S4 (3)
7	KEY
8	R1 (4)
9	R2 (4)
10	S1 (4)
11	S2 (4)
12	S3 (4)
13	S4 (4)

TB3	
PIN #	WIRE (RES #)
1	KEY
2	R1 (5)
3	R2 (5)
4	S1 (5)
5	S2 (5)
6	S3 (5)
7	S4 (5)
8	R1 (6)
9	R2 (6)
10	S1 (6)
11	S2 (6)
12	S3 (6)
13	S4 (6)

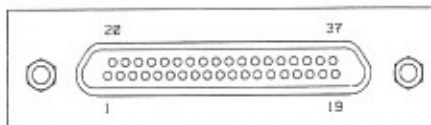
TB4	
PIN #	WIRE (RES #)
1	R1 (7)
2	R2 (7)
3	KEY
4	S1 (7)
5	S2 (7)
6	S3 (7)
7	S4 (7)
8	R1 (8)
9	R2 (8)
10	S1 (8)
11	S2 (8)
12	S3 (8)
13	S4 (8)

2.2.3. Digital I/O Wiring:

Please see the wiring chart and connector diagram on this page.

The digital I/O wiring should use shielded cable like Autotech's CBL-37S22-DCxxx. The shield is unterminated at the connector end and should be connected to "signal common" at the programmable logic controller (PLC). "Signal

Connector J1



Connector for logic input/output on front panel of DMS Series Decoder

Figure 6

DMS Series Decoder Wiring Table (for Connector J1)					
See Note 1	See Note 2	Signal Description	BCD Function	Binary Function	Gray Code Function
Pin #	Color				
1	Green Black	Sig Gnd (Customer VCC-)			
2	Red	Chan Select 0			
3	Orange	Chan Select 2			
4	Orange Black	N/C			
5	Green	N/C			
6	Blue	N/C			
7	Blue Black	Data Transfer (handshake)			
8	Black White		1 bit	bit 0	bit 0
9	White		4 bit	bit 2	bit 2
10	Black		10 bit	bit 4	bit 4
11	Red White		40 bit	bit 6	bit 6
12	Green White		100 bit	bit 8	bit 8
13	Red Green		400 bit	bit 10	bit 10
14	Blue White		1000 bit	N/C	N/C
15	Red Black	N/C			
16	White Black	N/C			
17	White Red	N/C			
18	Black Red	N/C			
19	Orange Red	N/C			
20	Blue Red	Customer VCC+			
21	Orange Green	Chan Select 1			
22	Black White Red	N/C			
23	Red Black White	N/C			
24	Green Black White	N/C			
25	White Black Red	Tristate (T)			
26	Orange Black White	N/C			
27	Blue Black White		2 bit	bit 1	bit 1
28	Black Red Green		8 bit	bit 3	bit 3
29	White Red Green		20 bit	bit 5	bit 5
30	Red Black Green		80 bit	bit 7	bit 7
31	Green Black Orange		200 bit	bit 9	bit 9
32	Orange Black Green		800 bit	bit 11	bit 11
33	Orange White Blue		2000 bit	N/C	N/C
34	Blue White Orange	N/C			
35	Black White Orange	N/C			
36	White Red Orange	N/C			
37	White Red Blue	N/C			

Note 1: Pin numbers apply to mating connector #ECM-37PIN-M11

Note 2: Wire colors and pin numbers apply to mating cable assembly #CBL-37S22-DCxxx

See How to Order, section 3, to specify these accessories.

common" at the PLC end should have a minimum of 100K ohm resistance to chassis ground. If "signal common" must be tied to earth ground, the DMS should be ordered with the optical isolation option. The DMS decoder can be mounted up to 200 feet away from the PLC using the recommended cable. For output and input interface to PLC or other external devices see input/output Interface Section in this catalog.

Notes:

1. When power is applied to the decoder the outputs go to a power-on code state for approx. 130 msec. For N Outputs: The output lines are all 1's for 130 msec.
2. The customer system design should take into account the power up state of the decoder.

3. Operation

3.1 PC Handshake:

The PC handshake circuitry allows synchronization of the continuously changing resolver information to the PLC, eliminating the possibility of capturing data bits in transition. This is accomplished by latching this resolver data at a known time after receiving a strobe from the PLC unit. This data remains latched until the completion of the next strobe and data transfer delay. The PC-handshake is triggered by either a low-to-high or high-to-low transition of the Data Transfer input and is non retriggerable during delay time. The transfer delay time from the PC- handshake transition to a valid resolver position data output is set to 100 μ sec.

There are no field adjustments in the DMS.

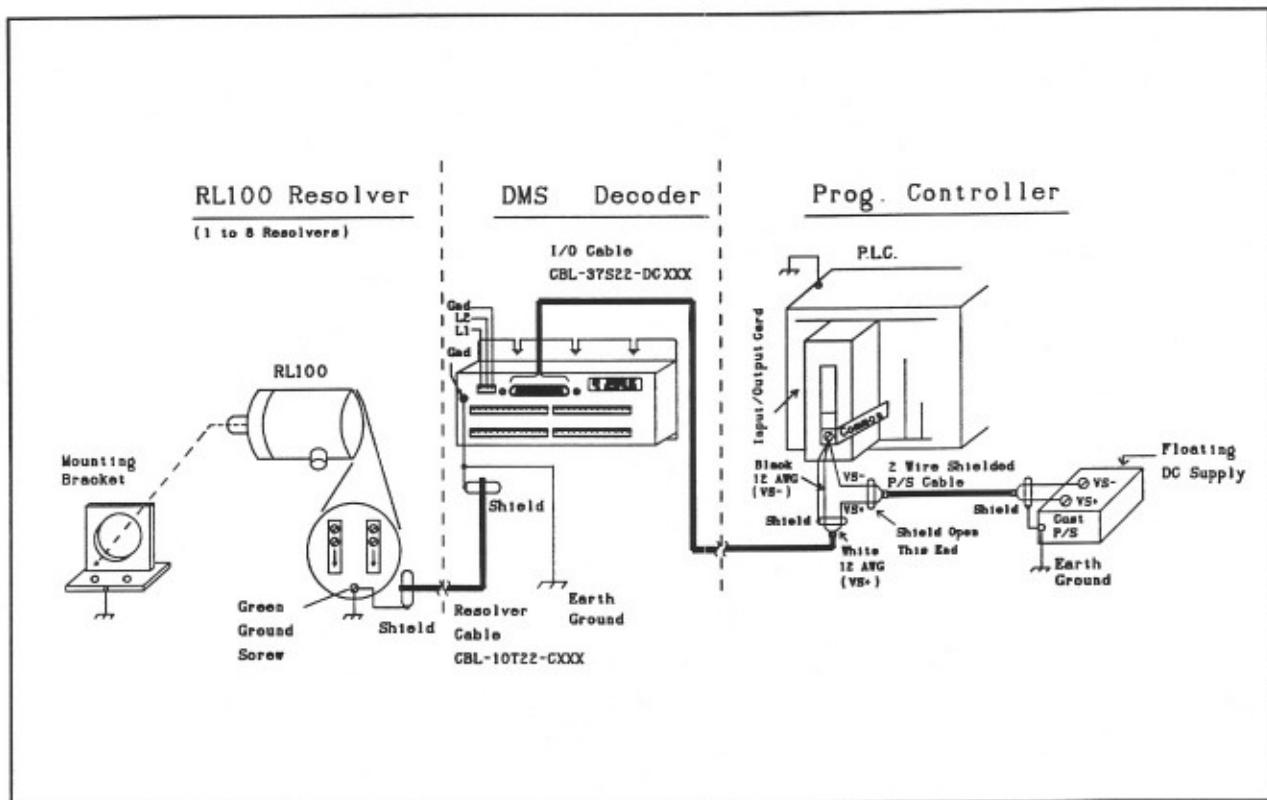
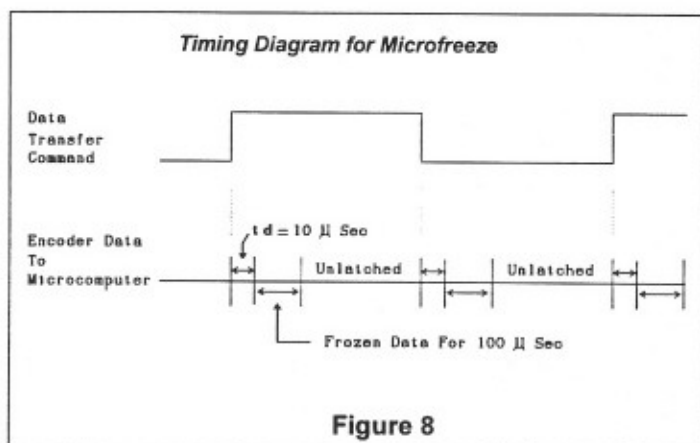


Figure 7

3.2 Microcomputer Synchronization (Microfreeze)

The standard "microfreeze" output-synchronization feature can be thought of as a "transparent PC Handshake". This feature is particularly useful when interfacing the DMS directly to a micro-computer where data transfer speeds are very fast.

When equipped with the microfreeze synchronization feature, the DMS will output decoded resolver position data continuously. When the micro-computer wants to read the resolver position data, it should toggle the DMS PC Handshake input. The PC Handshake input can toggle on either a high-to-low or low-to-high signal transition. Immediately after the PC Handshake input is toggled, the DMS will freeze (or hold) the current decoded resolver position output. The DMS will freeze this data for 100 μ sec ($\pm 10\%$) to allow the micro-computer time to read the resolver position without the possibility of output data changing. After this period of time, the DMS will resume outputting resolver position data continuously.

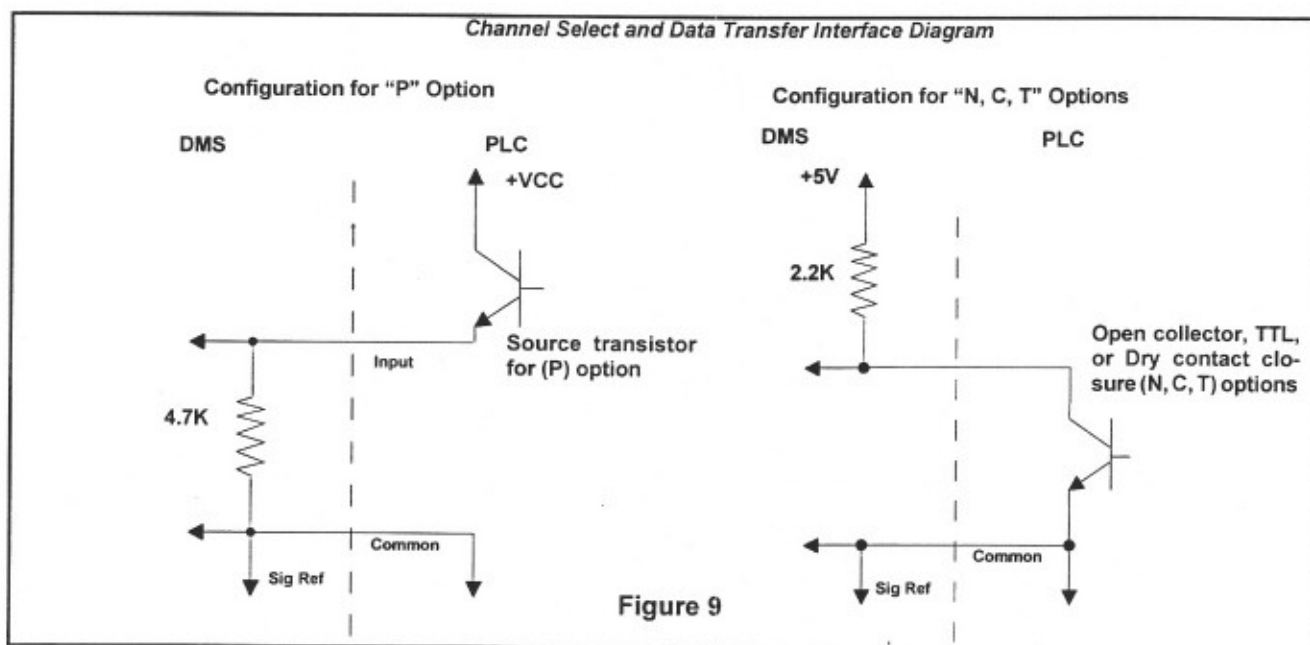


3.3 Resolver Channel Select

The DMS series decoders are multi-axis decoders. Depending upon the configuration, each DMS unit can decode the position of up to 8 resolvers. Each resolver is connected to a separate input channel (1-8) on the DMS decoder via the front panel Weidmuller connectors TB1-TB4.

The DMS decoder will output decoded position data for only one channel at a time. The data is output via logic input/output connector J1. Which channel's data is output is determined by the logic levels present at connector J1's "Channel Select" input pins (pins 2, 3 and 21). Refer to the following "Channel Select Table" for channel select coding.

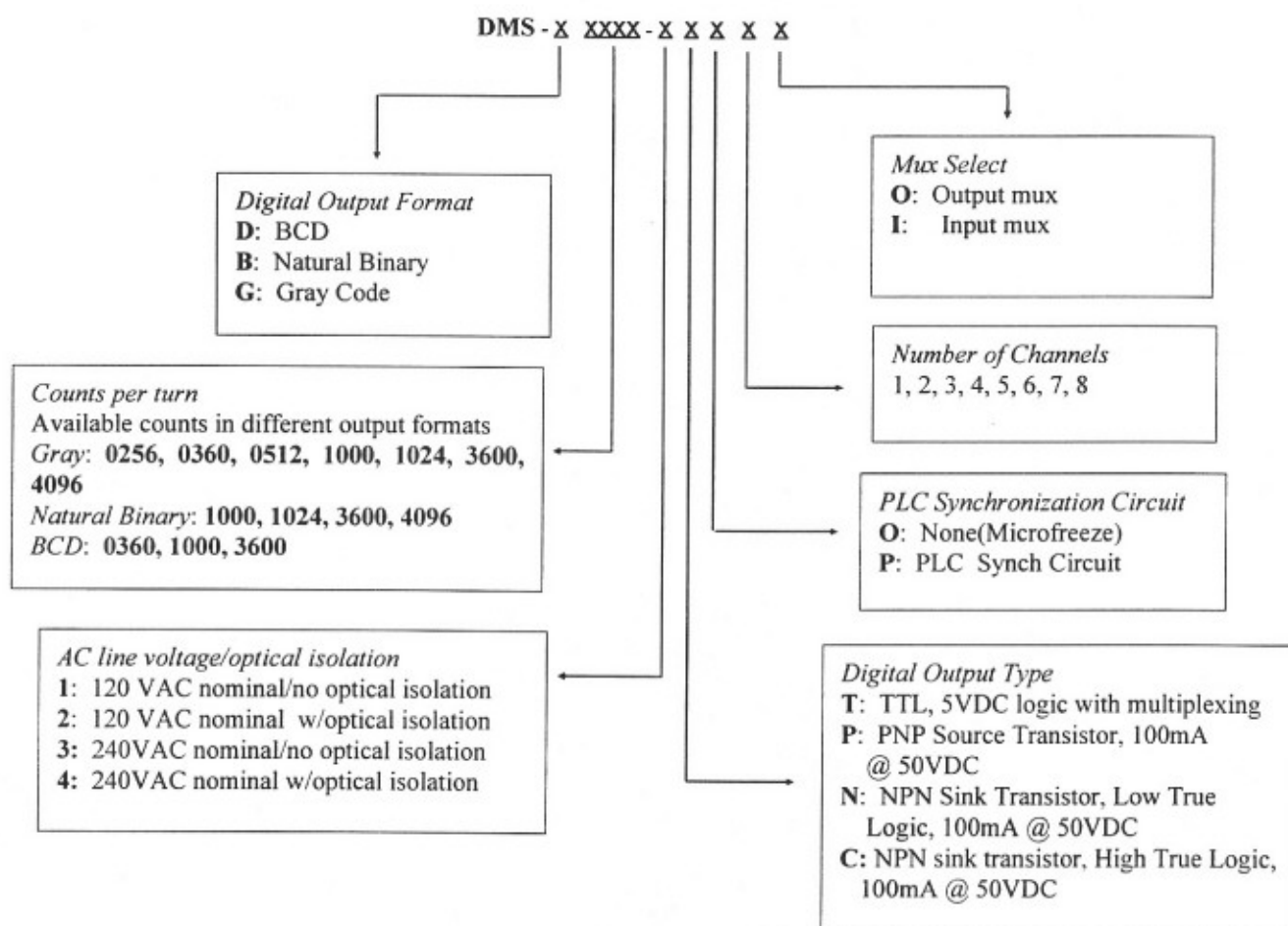
Channel Select Table			
Resolver Chan#	CS2	CS1	CS0
1	F	F	F
2	F	F	T
3	F	T	F
4	F	T	T
5	T	F	F
6	T	F	T
7	T	T	F
8	T	T	T



How to Order

The decoding system consists of a resolver-decoder, a position transducer (resolver), and appropriate interconnecting cables.

1. Multi-axis single-turn resolver decoder



2. Position Transducers

Select appropriate single turn resolver model RL100, E6R, E7R, E8R or RL101. See position transducer section for How to Order Information.

3. Accessories

3.1 Output Cables from decoder to PLC or other devices

CBL-37S22-DCxxx 37 conductor cable for digital output wiring with overall foil shield and 37 pin sub "D" connector on one end
xxx = Standard length in feet (10, 25, 50 and in increments of 50 feet)

ECM-37PIN-M11 37 pin sub "D" male connector (mates with the connector on DMS)

3.2 Other Accessories

For ordering resolver to decoder cable, mounting bracket, flexible shaft coupling etc. see the Position Transducer Section.

WARRANTY

Autotech Corporation and MC Technologies warrant their products to be free from defects in materials or workmanship for a period of one year from the date of shipment, provided the products have been installed and used under proper conditions. The defective products must be returned to the factory freight prepaid and must be accompanied by a Return Material Authorization (RMA) number. The Company's liability under this limited warranty shall extend only to the repair or replacement of a defective product, at The Company's option. The Company disclaims all liability for any affirmation, promise or representation with respect to the products.

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